Page 2 - MCU
MCU, Boot Options, Coin Battery
SPIFI Data Flash, JTAG

Page 3 - BASE
Gadgeteer Sockets, User LEDs, User Push Button, Reset Push Button, USB Device Port, 3.3V Power Supply

Page 4 - BAM200E
Gadgeteer Sockets, Ethernet
XBEE Interface, MicroSD Card
5.0V Power Supply

Block Diagram

- KUX Gadgeteer Socket
- OSX Gadgeteer Socket
- IPSU Gadgeteer Socket
- AOP Gadgeteer Socket
- AIY Gadgeteer Socket
- J6 USB Device micro USB Conn.
- RESET & USER Push Buttons
- USER LEDs
- Quad SPI Data Flash
- SPIFI
- DAC, SSP0, UART0, SGPIO
- ADC, DAC, PWM
- ADC, I2C1
- USB0
- P2.7
- PLL
- SPIFI Data Flash, JTAG
- USB1, I2C0
- GPIO
- SSP1, UART2
- SD Card Interface
- Real Time Clock/Calendar
- JTAG Connector
- PHY Ethernet Connector
- CR2032 Coin Cell Holder
- CORTEX M JTAG Connector

Power Supply Block Diagram

- TPS2115A Power MUX
- J4 USB0 micro USB Conn.
- J6 USB1 micro USB Conn.
- DIODE
- ADJ REG
- VR5
- P Channel FET
- Power Jack or Screw Terminal
- DIODE
- 3.3V REG
- 3.3V RAIL
- 5.0V RAIL
- SHOWN WITH POWER APPLIED TO POWER JACK

Revision Changes
B Corrected Boot mode pins, added ground test point, changed pin 5 of socket 3 to ADC0_2 from ADC0_0, added trace for debugging, changed SSP1_CLK from PF_4 to CLK0, added USB1 micro SD connector and USB power mux